

Temperature Compensated Crystal Oscillators



CMOS OUTPUT MINIATURE (5x3.2x1.2 mm) TCXO IN LCC PACKAGE - TC53C Series

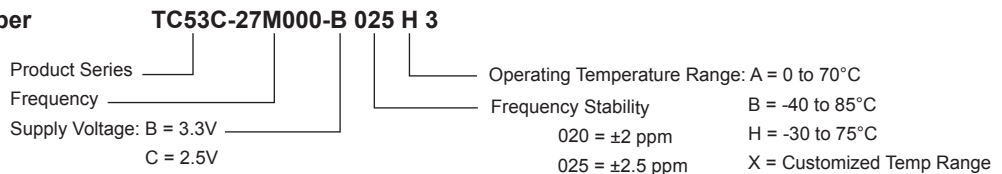
FEATURES

- RoHS Compliant (Pb-Free), Tight Stability, Wide Frequency Range
- CMOS Output with Tri-state function
- Leadless Chip Carrier (LCC) Miniature Small Package, Industry de factor Standard Footprint
- Low Phase Noise, Low Jitter and Low Power Consumption

SPECIFICATIONS

| | |
|-------------------------------------|---|
| Frequency Range | 4.000 MHz to 54.000 MHz |
| Input Voltage (Vcc) | 1.8 - 3.3 VDC; B = 3.3V ± 10%; C = 2.5VDC ± 10%; D = 1.8VDC ± 10% |
| Input Current | 7 mA Maximum / 1 uA Max Standby |
| Storage Temperature | -40°C to 125°C |
| Frequency Stability vs Temp. | 020 = ±2 ppm; 025 = ±2.5 ppm (not all combination is available) |
| Temperature Range | A = 0°C to 70°C; B = -40°C to 85°C; H = -30°C to 75°C |
| Standard Stability | 025H = ±2.5 ppm / -30°C to 75°C |
| Frequency Stability vs Vcc | ±0.2 ppm Maximum / Vcc ± 10% |
| Frequency Stability vs Load | ±0.2 ppm Maximum / 15 pF ±10% |
| Aging | ±1 ppm Maximum per year @25°C |
| Output Load | CMOS 15 pF |
| Logic "1" / Logic "0" Level | 0.9Vcc Minimum / 0.1Vcc Maximum |
| Rise/Fall Time (Tr/Tf) | 5 ns Maximum |
| Start-up time | 10 ms Maximum |
| Duty Cycle | 3 = Tristate 55/45% |
| Phase Noise (typ) | -145 dBc/Hz at 10KHz |
| Jitter (typ) | 3 ps, 1 Sigma RMS |
| Tristate Function | Input (Pin 1) High (> 0.7Vcc) or open: Output (Pin 3) active Input (Pin 1) Low (< 0.3Vcc): Output disabled in high impedance |

Creating a Part Number



OUTLINE DRAWING

