



SINEWAVE OUTPUT HIGH STABILITY VCXO IN 14 PIN DIP PACKAGE- VC14S Series

FEATURES

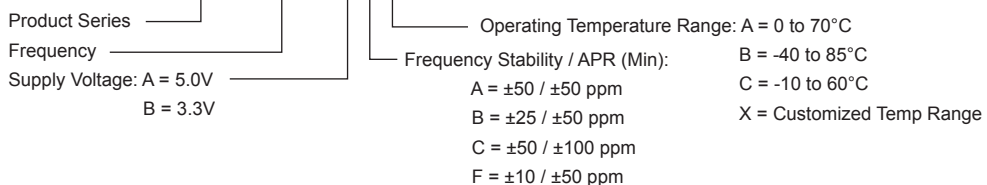
- RoHS Compliant (Pb-Free), Wide Frequency Pulling Range (± 150 ppm, etc.)
- Very Low Phase Jitter with Fundamental or 3rd O/T Crystal Design
- 5 VDC or 3.3 VDC Option, Industry Standard Lead Spacing
- Sealed UM-1 Crystal Inside for High Stability: ± 10 ppm / 0°C to 70°C is available

SPECIFICATIONS

Frequency Range	6 MHz to 190 MHz
Input Voltage (Vcc)	A = +5 VDC $\pm 5\%$; B = +3.3 VDC $\pm 5\%$
Input Current (Max.)	20 mA (to 25 MHz); 30 mA (to 50 MHz); 60 mA (to 125 MHz); 70 mA (to 190 MHz)
Control Voltage (Vc)	+2.5V ± 2.0 V for 5.0V part; +1.65V ± 1.5 V for 3.3V part
Storage Temperature	-55 $^{\circ}\text{C}$ to 125 $^{\circ}\text{C}$
Frequency Stability / APR (Min)	A = ± 50 / ± 50 ppm; B = ± 25 / ± 50 ppm; C = ± 50 / ± 100 ppm; F = ± 10 / ± 50 ppm
Temperature Range	A = 0°C to 70°C ; B = -40°C to 85°C ; C = -10°C to 60°C
Standard Stability / Pullability	BA = ± 25 ppm / 0°C to 70°C , Absolute pull range (APR): ± 50 ppm Minimum
Aging	± 3 ppm Max per year
Output Load	50 Ohms
Output Waveform	Sine wave
Output Level	0 dBm Typ for 3.3V part; 10 dBm Typ for 5.0V part
Start-up time	10 ms Maximum
Phase Jitter (RMS, 1 Sigma)	1 ps Maximum for $f_j > 1\text{kHz}$; 0.3 ps Typical for $f_j = 12\text{kHz}$ to 20MHz
Modulation Bandwidth	10 kHz Minimum at -3 dB
Linearity / Slope	$\pm 10\%$ Maximum of best straight line fit / Positive
Input Impedance	10 kOhms Minimum
Setability at Fnom, 25$^{\circ}\text{C}$	+2.5V ± 0.5 V for 5.0V part; +1.65V ± 0.4 V for 3.3V part

Creating a Part Number

VC14S-44M736-A F C



OUTLINE DRAWING

