



**32.768KHz CMOS CLOCK OSCILLATOR IN 3.2x2.5x0.9 mm SMD PACKAGE : XO32-32K768**

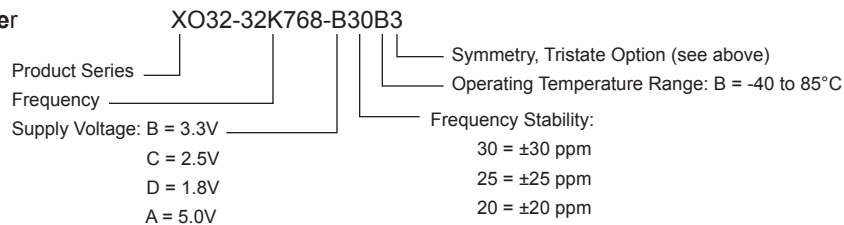
**FEATURES**

- RoHS Compliant (Pb-Free)
- Tight Stability over Wide Temperature Range (Divided Circuit)
- Popular 32.768KHz Square Wave Output, Tri-state Enable/Disable Standard
- Leadless Chip Carrier (LCC) Ultra Small Package

**SPECIFICATIONS**

<b>Frequency</b>	32.768 KHz
<b>Input Voltage (Vcc)</b>	A = +5.0VDC $\pm$ 5%; B = +3.3VDC $\pm$ 5%; C = +2.5VDC $\pm$ 5%; D = +1.8VDC $\pm$ 5%
<b>Input Current</b>	100 $\mu$ A Maximum (80 $\mu$ A Typ)
<b>Storage Temperature</b>	-55°C to 125°C
<b>Overall Frequency Stability</b>	30 = $\pm$ 30 ppm; 25 = $\pm$ 25 ppm; 20 = $\pm$ 20 ppm
<b>Temperature Range</b>	B = -40°C to 85°C
<b>Standard Stability</b>	30B = $\pm$ 30 ppm / -40°C to 85°C
<b>Electric Option (Symmetry)</b>	1 = Tristate 60/40%; 3 = Tristate 55/45%
<b>Output Load</b>	HCMOS: 15 pF load
<b>Logic "1" / Logic "0" Level</b>	0.9Vcc Minimum / 0.1Vcc Maximum
<b>Rise/Fall Time (Tr/Tf)</b>	6 ns Maximum
<b>Start-up time</b>	1 ms Maximum
<b>Phase Jitter (RMS, 1 Sigma)</b>	1 ps Max for fj > 1kHz; 0.3 ps Typical for fj = 12KHz to 20MHz
<b>Tristate Function</b>	Input (Pin 1) High (> 0.7Vcc, or 2.2V if Vcc=5V) or open: Output (Pin 3) active Input (Pin 1) Low (< 0.3Vcc, or 0.8V if Vcc=5V): Output disabled in high impedance
<b>Output Disabled Time</b>	100 ns Maximum
<b>Output Enable Time</b>	1 ms Maximum (or 100 ns Maximum as an option)

**Typical Part Number**



**OUTLINE DRAWING**

