

HCMOS/ACMOS/TTL COMPATIBLE SMD CLOCK OSCILLATORS - XO53 Series

FEATURES

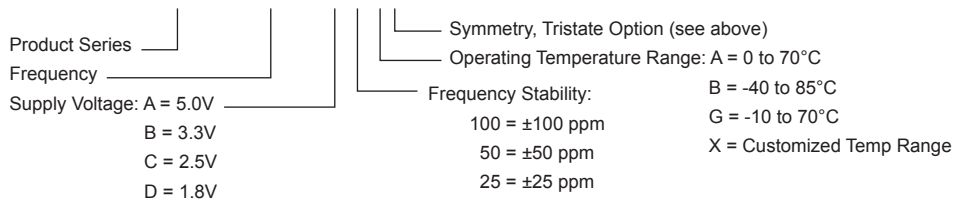
- RoHS Compliant (Pb-Free), Industry Standard Pin-out Spacing
- Very Low Phase Jitter with Fundamental or 3rd Overtone Crystal Design
- Tri-state Enable/Disable Standard; 5V and 3.3V Option
- Leadless Chip Carrier (LCC) Ultra Small Package (5x3.2x1.0 mm)

SPECIFICATIONS

Frequency Range	1.8432 MHz to 75.00 MHz (5V), to 156.00MHz (3.3V, 2.5V)
Input Voltage (Vcc)	A = +5 VDC $\pm 10\%$; B = +3.3 VDC $\pm 10\%$; C = +2.5 VDC $\pm 10\%$; D = +1.8 VDC $\pm 10\%$
Input Current	60 mA Maximum, depending on supply voltage, frequency and output load
Storage Temperature	-55°C to 125°C
Overall Frequency Stability	100 = ± 100 ppm; 50 = ± 50 ppm; 25 = ± 25 ppm
Temperature Range	A = 0°C to 70°C; B = -40°C to 85°C; D = -20°C to 70°C; G = -10°C to 70°C
Standard Stability	100A = ± 100 ppm / 0°C to 70°C
Electric Option (Symmetry)	1 = Tristate 60/40%; 3 = Tristate 55/45%
Output Load	HCMOS: Drive up to 50 pF load; TTL: Drive up to 10 TTL gates
Logic "1" / Logic "0" Level	0.9Vcc Minimum / 0.1Vcc Maximum
Rise/Fall Time (Tr/Tf)	10 ns Maximum, depending on frequency and output load
Start-up time	10 ms Maximum
Phase Jitter (RMS, 1 Sigma)	1 ps Max for $f_j > 1$ kHz; 0.3 ps Typical for $f_j = 12$ kHz to 20 MHz
Tristate Function	Input (Pin 1) High ($> 0.7V_{cc}$, or 2.2V if $V_{cc}=5V$) or open: Output (Pin 3) active Input (Pin 1) Low ($< 0.3V_{cc}$, or 0.8V if $V_{cc}=5V$): Output disabled in high impedance
Output Disabled Time	100 ns Maximum
Output Enable Time	10 ms Maximum (or 100 ns Maximum as an option)

Creating a Part Number

XO53-25M000-B50A3



OUTLINE DRAWING

